

Applicant : David B. Minturn
Serial No. : 10/748,415
Filed : December 30, 2003
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Attorney's Docket No.: INTEL-049PUS
Intel Docket No. P17385

REMARKS

Claims 1 to 30 are pending in this application. Claims 1, 8, 10, 17 and 22 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Applicants note that the Office Action dated October 9, 2007 does not reject claim 12 in any of the pages of the Office Action other than the coversheet. Even though the Examiner has indicated that claim 12 is rejected he has failed to provide any support whatsoever much less a *prima facie* rejection. Applicants assume that claim 12 is allowable. If claim 12 is not allowable, Applicants respectfully submit that the next office action *must* be a non-final action since claim 12 has not been properly rejected so that any rejection with respect to claim 12 in the next action would be a new ground of rejection (see MPEP §706.07(a)).

The Examiner objected to the drawings. Based on the foregoing specification amendments Applicants respectfully request withdrawal of the drawing objections.

The Examiner objected to the specification, alleging that it is inconsistent. Applicants respectfully submit that the Examiner has misread the specification. In particular, the Examiner has erroneously equated "pages" to "entries." Applicants encourage the Examiner to call Applicants' undersigned representative for further explanation if the Examiner is still confused. Based on the foregoing specification amendments Applicants respectfully request withdrawal of the specification.

The Examiner has objected to claims 10 and 17. Based on the foregoing claim amendments Applicants respectfully request withdrawal of the claim objections.

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Claims 1 to 30 were rejected under 35 U.S.C. § 112, second paragraph. The Examiner alleges that neither the disclosure nor the claim defines the term “a cache line determinator.” Applicants submit that the term as used in the claim element defines the term to one of ordinary skill in the art and is further defined on paragraph [0018] of Applicant's specification. Applicants respectfully submit that an “applicant is entitled to be his or her own lexicographer” and “where an explicit definition is provided by the applicant for a term, that definition will control interpretation of the term as it is used in the claim. *Toro Co. v. White Consolidated Industries Inc.*, 199 F.3d 1295, 1301 (Fed. Cir. 1999)” (see MPEP §2111.01 Part IV). The Examiner submits that a “context” and a “cache line” are indefinite. Applicants respectfully submit that these terms are terms of art and that one of ordinary skill in the art would understand the meaning of these terms. Based on the foregoing remarks and claim amendments Applicants respectfully request withdrawal of the §112 rejections.

Claims 1 to 3, 6, 7, 10 to 12, 15 to 25 and 30 were rejected under 35 U.S.C. § 102(b) as being anticipated by Boucher et al. (U.S. Patent Number 7,174,393 hereinafter “Boucher”). Claims 4, 5, 8, 9, 13, 14 and 26 to 29 were rejected under 35 U.S.C. § 103(a) as being obvious over Boucher in view of Bryg et al (U.S. Patent Number 6,393,544 hereinafter “Bryg”).

Claim 1 is directed to a network interface controller. The network interface controller includes a hashing logic to generate a hashing value from a packet received from a network. The received packet has a context associated therewith. The network interface controller also includes a hash table pages table and a context table pages table for storing host memory

physical page addresses of a host hash table and a host context table, respectively and a cache line determinator in communication with the host and the hashing logic. The cache line determinator is configured to associate the hash value with a host hash table cache line and a host context table cache line in a host memory using the hash table pages table and the context table pages table, respectively.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, Boucher does not disclose or suggest that a cache line determinator is configured to associate the hash value with a host hash table cache line and a host context table cache line in a host memory using the hash table pages table and the context table pages table, respectively.

The Examiner has indicated a cache line determinator is the communication processing device (CPD) 30 in FIG. 1 (see page 6 of the Office Action). However, Boucher does not indicate that the CPD 30 associates the hash value with a host hash table cache line and a host context table cache line in a host memory much less associates the hash value with a host hash table cache line and a host context table cache line in a host memory using the hash table pages table and the context table pages table. Furthermore, the Examiner has failed to show support that the CPD 30 "associates" as recited in claim 1.

Moreover, the Examiner has indicated that the hashing logic is receive logic 32. However, the receive logic 32 is a part of CPD 30 which the Examiner has called the cache line determinator. Applicants respectfully point out that this inconsistent and illogical since Applicants have distinctly claimed the hashing logic and the cache line determinator as separate elements. Therefore, Boucher does not disclose or suggest that a cache line determinator is

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configured to associate the hash value with a host hash table cache line and a host context table cache line in a host memory using the hash table pages table and the context table pages table, respectively.

Based on the foregoing reasons, Applicants request withdrawal of the art rejection.

Claims 8 and 22 have corresponding features to claim 1. Applicants submit that the Boucher reference should also be withdrawn with respect to claims 8 and 22 for at least the same reasons as claim 1.

Moreover, the Examiner has indicated that claim 17 is rejected for the same reasons as claim 10. The Examiner indicated that claim 10 is rejected for the same reasons as claim 1. Applicants respectfully submit that "computing a host context table cache line in the host memory using the hash value and using a context table pages table containing host memory physical page addresses of a host context table" (emphasis added) as recited in claims 10 and 17 is not recited in claim 1. Therefore, Applicants submit that the Examiner has failed to make a *prima facie* rejection since he has failed to address claims 10 and 17 as recited.

For at least the foregoing reasons, Applicants request withdrawal of the art rejections.

Applicants submit that all dependent claims now depend on allowable independent claims.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this

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paper should be construed as intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicants submit that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

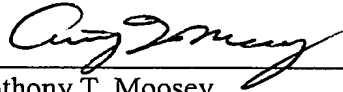
Applicants' attorney can be reached by telephone at (781) 401-9988 ext. 123.

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: INTEL-049PUS.

Respectfully submitted,

Date: _____

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